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Date: August 1, 2005

PATENT APPLICATION Attorney's Doc. No.: 2522-026 Client Ref. No.: AW8116US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Ji-Young KIM and Hyoung-Sub KIM

Serial No.:

10/649,262

Examiner:

Lindsay, Jr., Walter Lee

Filed:

August 26, 2003

Art Unit:

2812

Confirmation No.

7198

For:

INTEGRATION METHOD OF A SEMICONDUCTOR DEVICE

HAVING A RECESSED GATE ELECTRODE

TRANSMITTAL LETTER

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

| \boxtimes | Publicatio | n (\$3 | 00) and | Issue | (\$140 | 0.00 | large) | fees |
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In connection with issuance of a patent: \bowtie

Supplemental Declaration \boxtimes PTO Form 85B

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Customer No. 20575

Respectfully submitted, MARGER JOHNSON & McCOLLOM, P.C.

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APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

The Examiner's statement of reasons for Allowance lists some but not all, of the reasons for allowance. The Examiner recited portions of a few of the allowed independent claims and stated that the prior art, either singly or in combination, fails to anticipate or render obvious the limitations of the recited portions. The applications note that it is a well known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

Customer No. 20575

Respectfully submitted,

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I hereby certify that this correspondence is being transmitted to the U.S. Patent & Trademark Office via facsimile number (703) 746-4000 on August 1, 2005.

Stormi R. Davis

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